

## 2011 CS Mantech Workshop Schedule

7:30 – 8:30 am	<b>Registration</b>
8:30 – 8:45 am	<b>Welcome and introductions</b>
8:45 – 9:45 am	<b>Workshop Session 1: The Use of Process Simulation Tools for the Development of Compound Semiconductor Devices and Integrated Circuits</b> Charles Fields, Agilent Technologies
9:45 – 10:45 am	<b>Workshop Session 2: Modeling for Process Engineers</b> Pete Zampardi, Skyworks Solutions, Inc.
10:45 – 11:00 am	<b>Break</b>
11:00 – 12:00 pm	<b>Workshop Session 3: Electronic Design Automation (EDA) Tutorial FET/pHEMT Modeling and Characterization</b> Paul Litzenberg, TriQuint Semiconductor
12:00 – 1:00 pm	<b>Lunch</b>
1:00 – 2:00 pm	<b>Workshop Session 4: RF and Low Noise Measurements for III-V Devices</b> Marcel Tutt, Freescale
2:00 – 3:00 pm	<b>Workshop Session 5: Passive and EM simulation</b> Michael Thompson, Agilent Technologies
3:00 – 3:15 pm	<b>Break</b>
3:15 – 4:15 pm	<b>Workshop Session 6: Compound Semiconductor Process Technologies, Design Kits, and Volume Production</b> Hongxiao Shao, Skyworks Solutions, Inc.
4:15 pm	<b>End of Program</b>
6:00 – 9:00 pm	<b>Exhibits Reception</b>